

# Session U

## Solid-State Oscillators

**Chairman:**

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This session represents the advances in the field of microwave oscillator design and applications. The first paper deals with the oscillator phase noise simulation using harmonic balance technique. The next paper is a DDS based injection locked oscillator. The third paper describes an interesting application of 42 GHz and 7 GHz ETDRO in HDTV. The last paper describes an electronically tunable fundamental MMIC oscillator at 29 GHz.

**10:30 a.m.-12:00 p.m., Wednesday, June 16, 1993**  
**Room 218/219**

